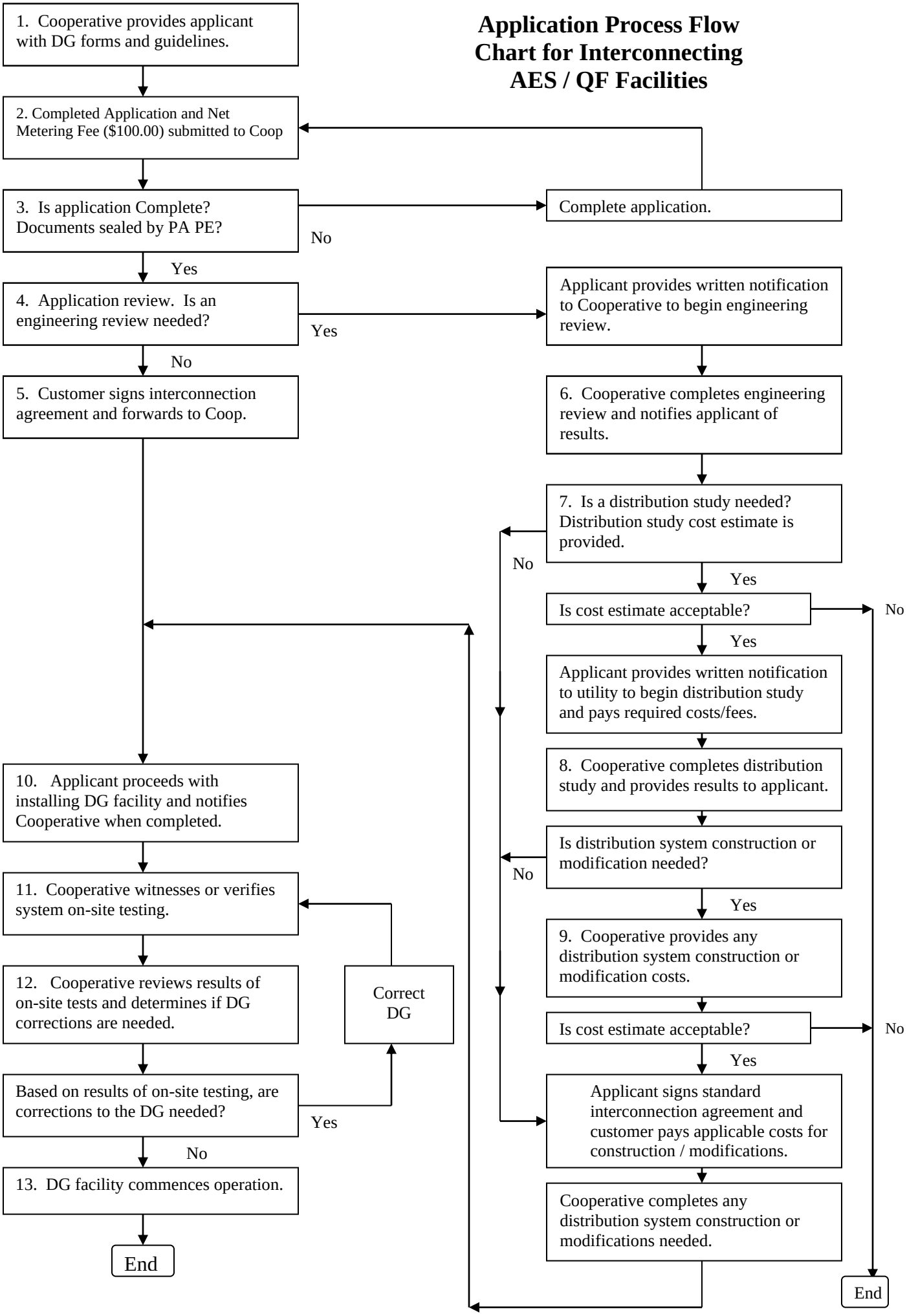Application Process Flow Chart for Interconnecting AES / QF Facilities